

**Project Title:** SysFix: Building an End-to-end Troubleshooting Solution for Large-scale Systems with Automated Failure diagnosis, Root cause analysis and Remediation(ELS)

**Sponsor:** Science and Engineering Research Board (SERB) (3rd and 4th Floor, Block II, Technology Bhavan, New Mehrauli Road, New Delhi 110016)

**Consolidated Compensation:** Upto Rs.35000(depending upon qualification & experience)

**Qualifications:** M.E./M. Tech in CSE/IT/Maths (with CS) and CGPA above 7.5 in B.Tech/BE onwards. Selected through a process described through any one of the following: a.

Scholars who are selected through National Eligibility Tests - CSIR-UGC, NET, Including lectureship (Assistant Professorship) and GATE. b. The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR. IIT, IISc. IISER, NISER etc. (2) Two years of research experience.

**Relevant Experience:**

**Application Fees:** NIL

**Age Limit:** Upper Age Limit is 32 Years. Relaxable upto 5 Years in the case of SC/ST/OBC/ Physically Handicapped/Visually Handicapped and female applicants

**Tenure:** 5 Months or the termination of the project, whichever is earlier.

**Position:** Senior Research Fellowship - Research

**PI:** Bivas Mitra

**Department:** Computer Science and Engineering

**Ref**

**No.:** IIT/SRIC/R/ELS/2024/114

**Ref Date:** 25-06-2024

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