Department of Electronics & Communication Engineering



National Institute of Technology Durgapur

Durgapur - INDIA

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NITD/ECE/SMDP/2018/Staff/WalkIn/01

January 10, 2018

WALK-IN-INTERVIEW FOR PROJECT POSTIONS In "Special Manpower Development Project- Chips to System Design (SMDP-C2SD)"

Applications are invited for the following positions under the SMDP-C2SD Project sponsored by MeitY, Govt. of India at NIT Durgapur

Interested candidates are required to attend the interview with original and attested copies of all academic and experience certificates (if any) with other testimonials on Jan 27, 2018 at 09.30 AM in the Department of ECE, NIT Durgapur.

Application form is available here: https://goo.gl/forms/BpSAaNGTbWdTDDeJ3

Details about the position(s):

Sl	Post	Qualification	Duration	Consolidated Salary (PM)
1	Project Faculty/ Temporary Lecturer	ME/MTech/PhD in the field of Microelectronics/VLSI/EC with experience in CAD tools and Chip design/Linux	Initially for the period of 1 year which may be extended	Rs 40,000/= (Per month)
2	Lab. Engineer	M. Tech/M.E. in Microelectronics/VLSI with experience in CAD tools	As above	Rs. 34,000/= (Per month)

Job Responsibilities: The selected candidates must be capable of doing the following:

To Design and Simulate the Chip/System assigned by the RC/appropriate agency.

Maintain/update the servers/workstations/PCs running various VLSI software in the Windows and Unix OS environments.

To conduct classes and help the students in the implementation of their projects in the area of VLSI/SoC

To prepare various documentation related to the projects

Any other works related to the project to be assigned from time to time.

Terms and Conditions:

- 1. The above position is purely contractual, initially for 1 year and renewable after every year till the project period is over.
- 2. No TA/DA will be paid for attending the interview.
- 3. Candidates have to appear for walk-in-interview with bio-data, passport photos, original certificates and attested photocopies of all certificates, mark sheets, degrees and testimonials. They are also required to apply online through the link mentioned earlier.
- 4. Reporting time at the Department of ECE, NIT Durgapur for the Interview: Jan 27, 2018 at 9:30 AM. Candidates reaching late will not be entertained.
- 5. Candidates may be allowed to enroll for PhD program subject to NIT Durgapur regulations.
- 6. Last date of filling online application is Jan 25, 2018.



National Institute of Technology Durgapur Durgapur-713209, INDIA Application Form for SMDP-C2SD Project Position (Temporary Project Posts)

Paste here a recent
Passport size
Photograph

1. Post Applied for:	Project Faculty (Temp	porary Lec	turer) / Lab Engineer	
2. Name of the Candi	date (BLOCK LETTERS):			
3. Father's Name (BI	LOCK LETTER):			
4. Mother's Name (B5. (a) Date of Birth (I(c) Marital Status:	LOCK LETTER): DD/MM/YYYY) Married/ Single (d) Category: SC	C/ST/OBC/	(b)Sex(Male/Female/Othor	er):
6. Previous Research/e	experience:			
7. Publication(s), if any 8. GATE/ NET (if any	y:): Qualified (Yes/No):Score: Specialization:			
9. Academic Qualification	tion: (Starting from Standard 10 o	or equivale	ent Examination)	
Name of Exam. Passed	Name of the School/College/ Institute/ University	Year of Passing	Discipline/ Specialization	Percentage of Marks/ CGPA
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10. (a) Address for Communication (BLOCK LETT				
(b) Contact No (Me	ob)			
(c) E-mail ID				
11. Name of the Refer	ree with email			-
12. Experience on Cao	dence/ Synopsys/ Mentor	r/ Linu	ux / Others/	(in years)
•	that the information furnished in f. If selected, I promise to abide b	* *	•	ıte.
Date: Place:			Full Signature of the A	pplication